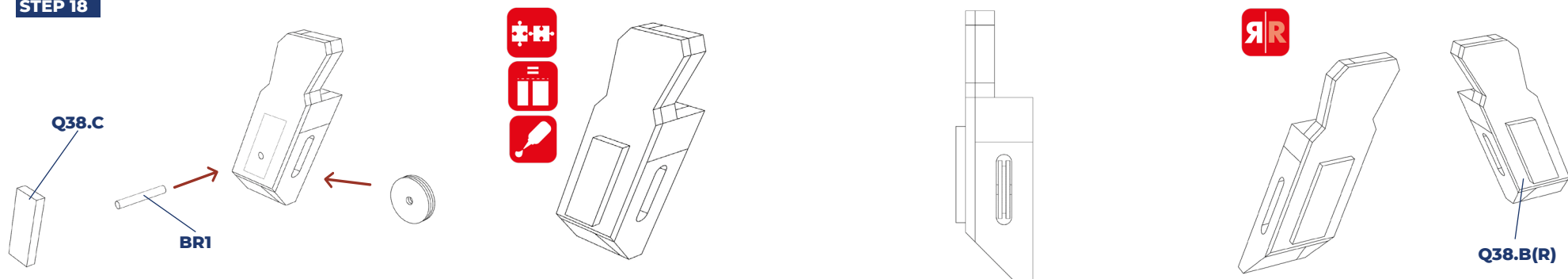
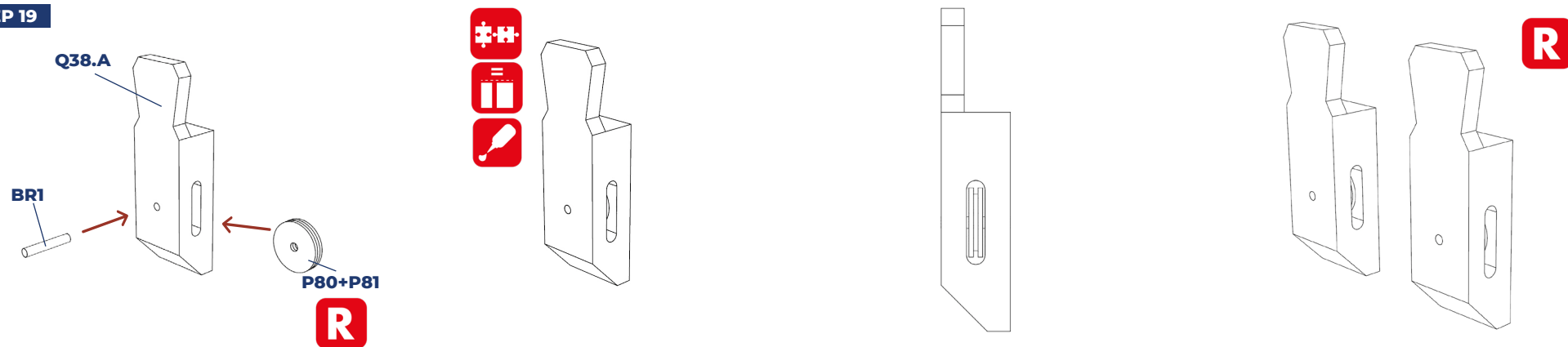


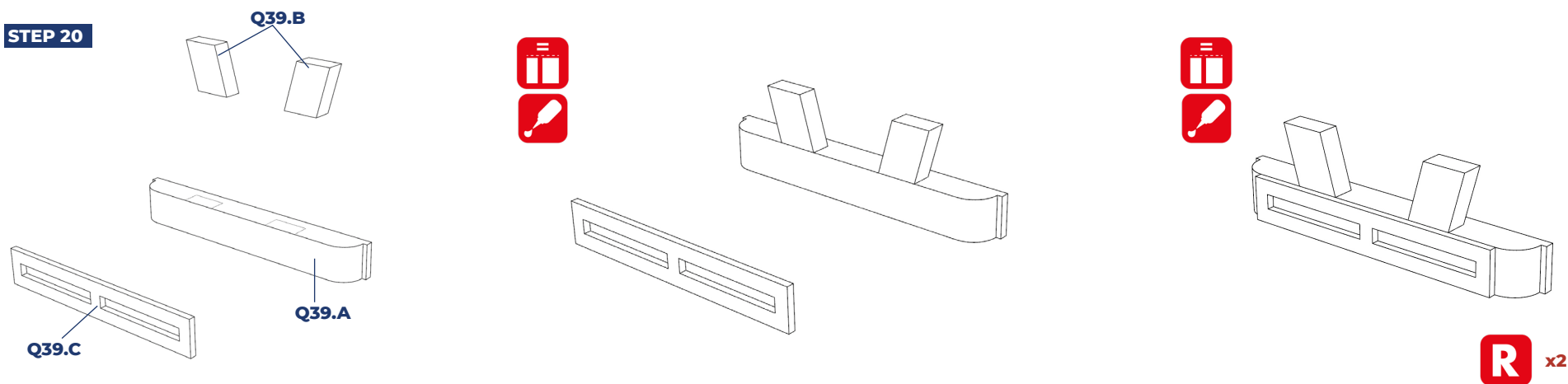
STEP 18



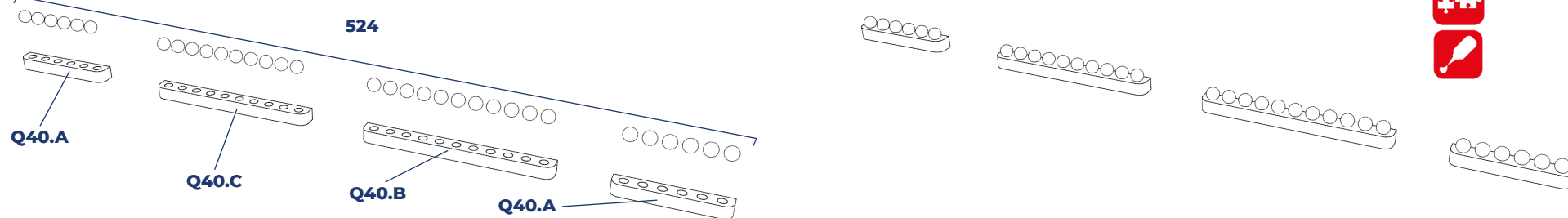
STEP 19



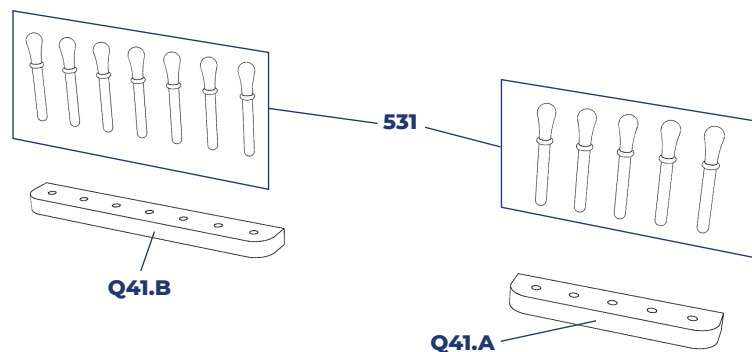
STEP 20



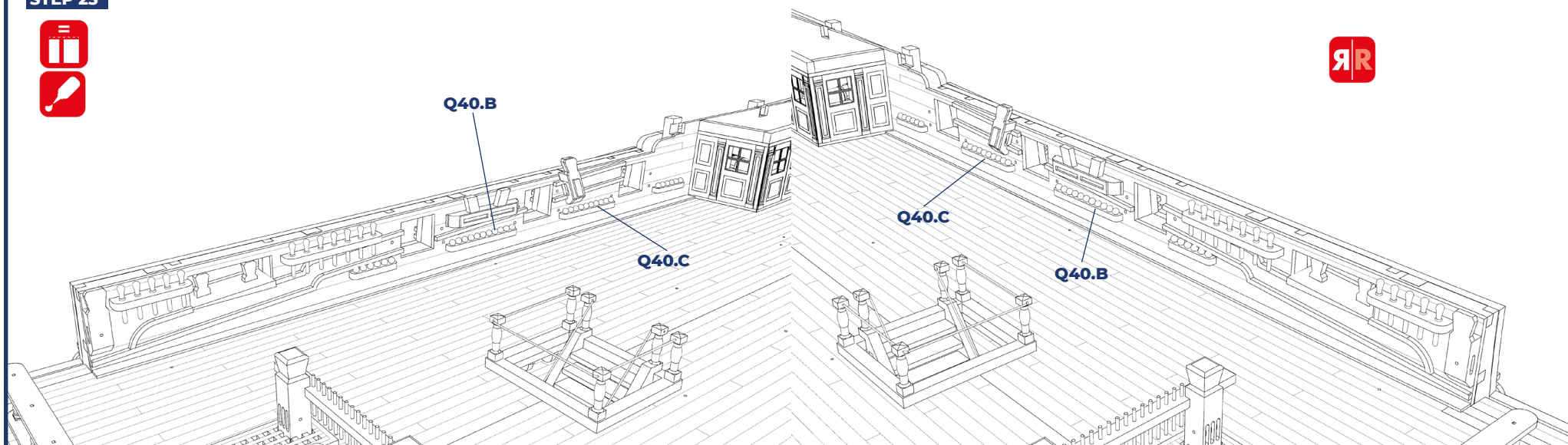
STEP 21



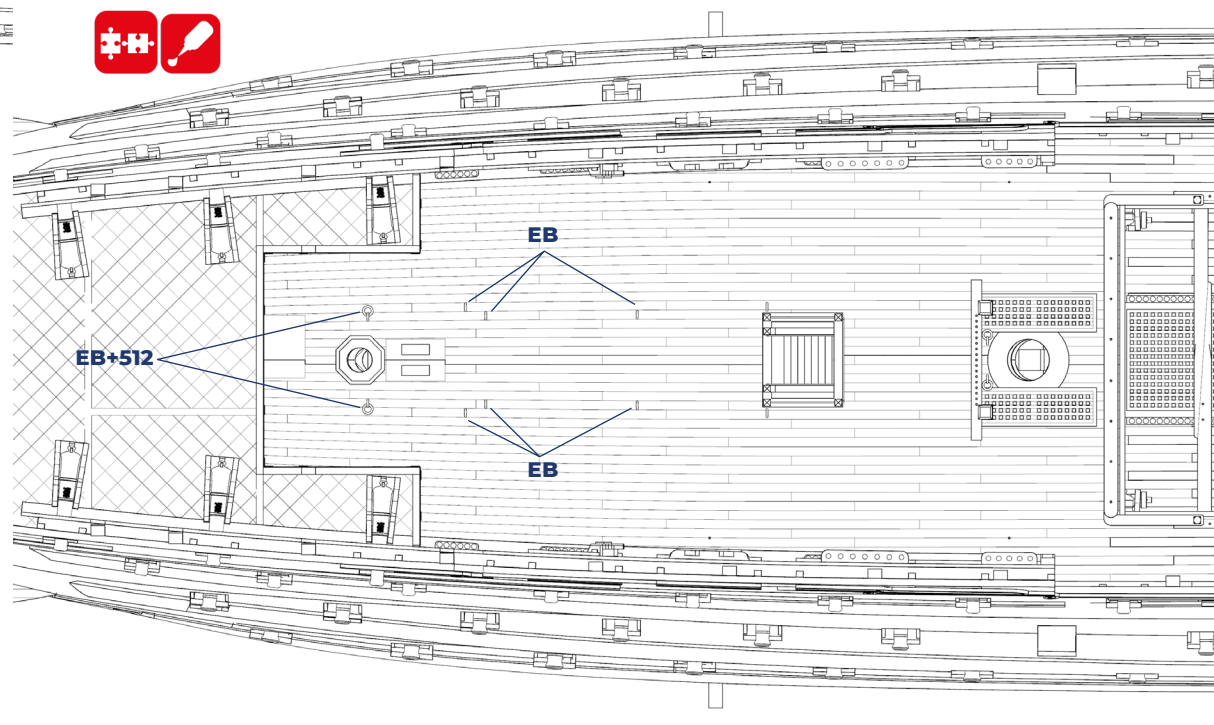
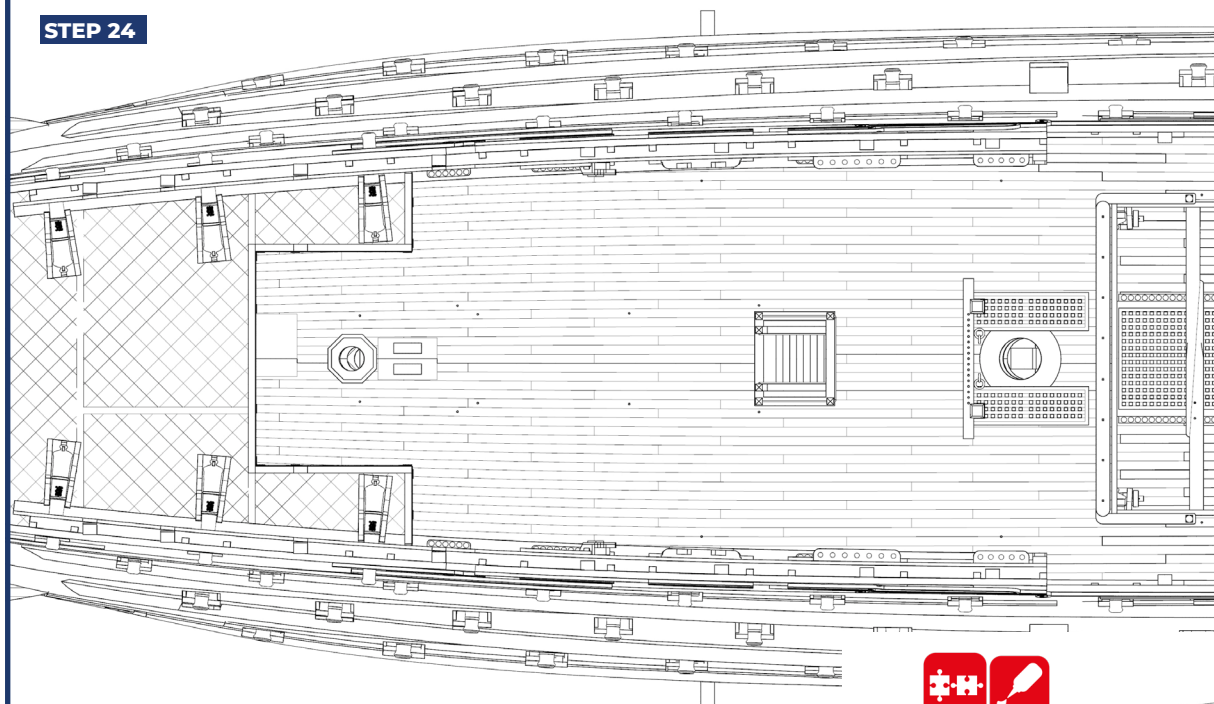
STEP 22



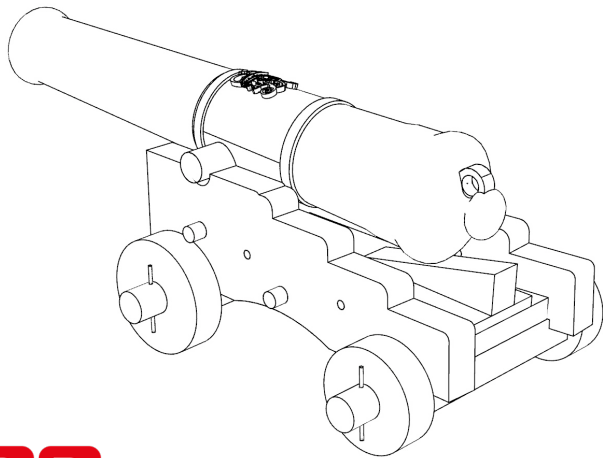
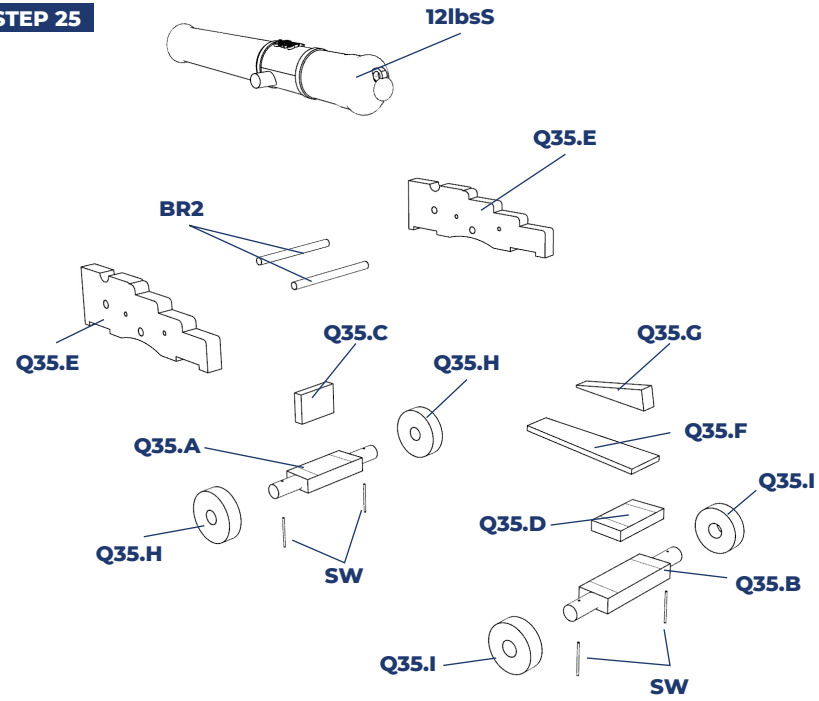
STEP 23



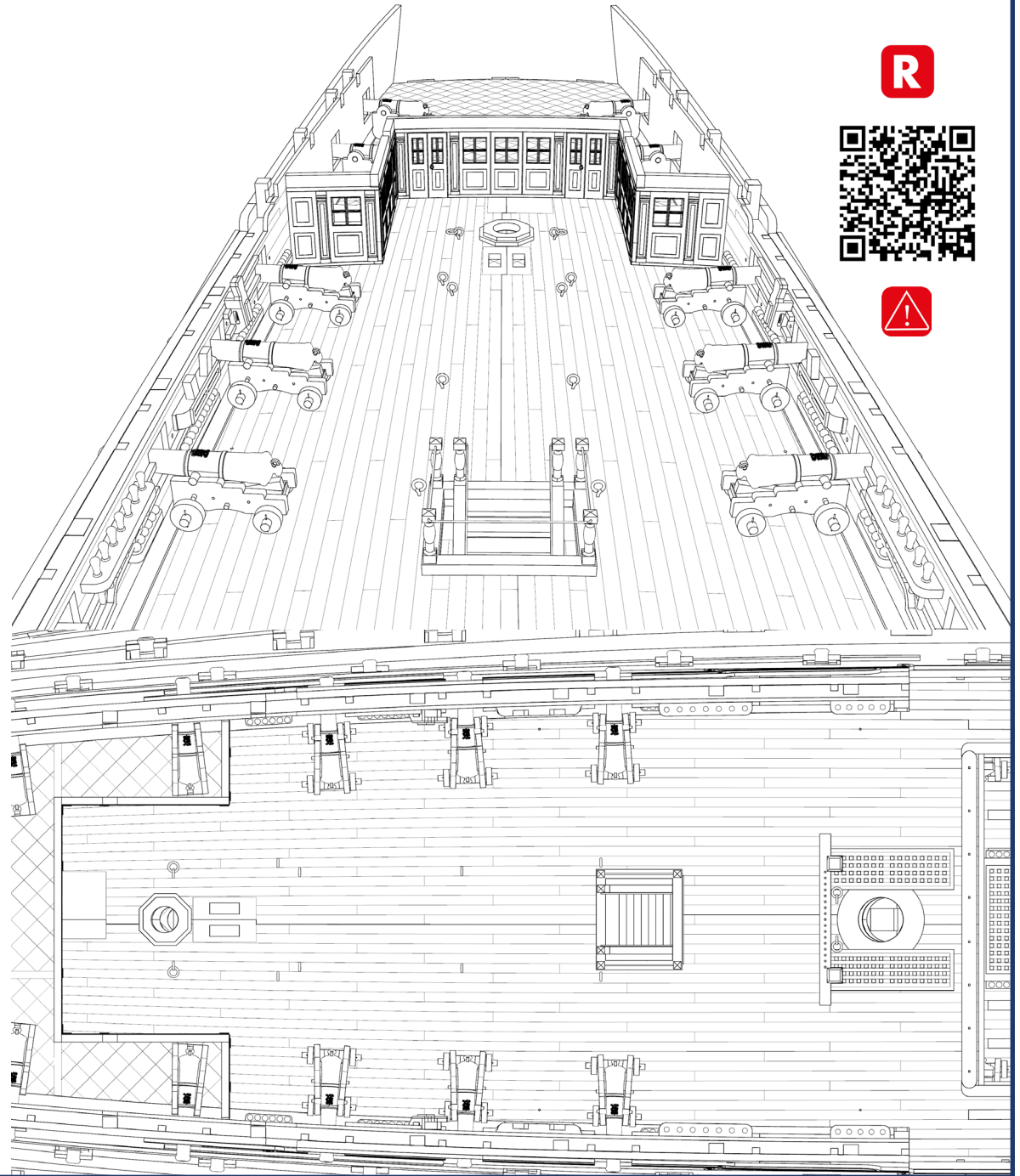
STEP 24



STEP 25



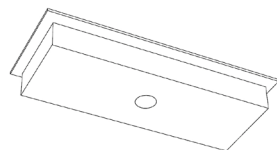
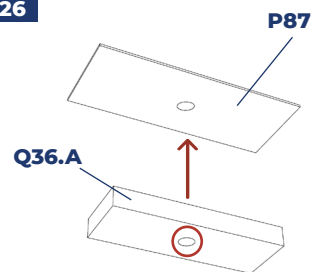
RR x6



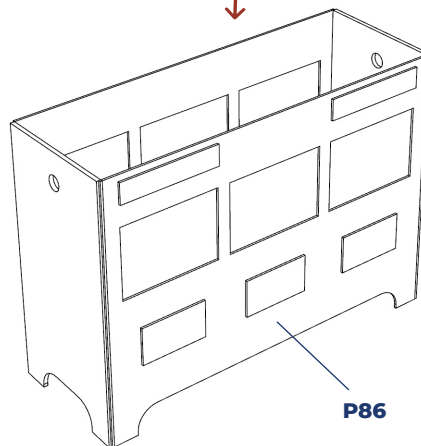
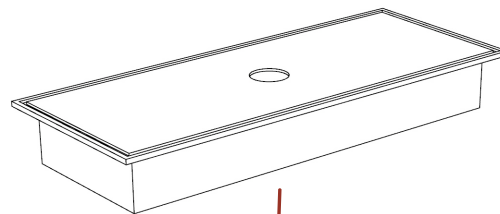
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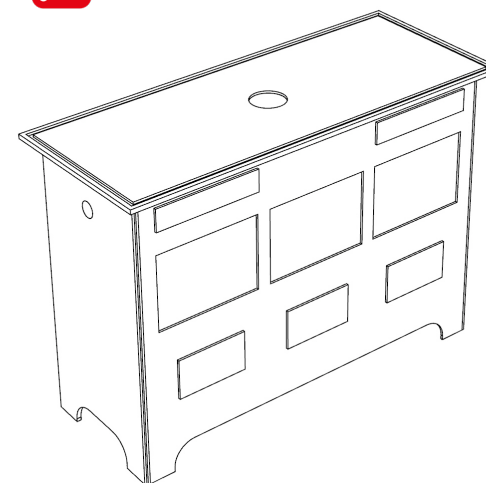
STEP 26



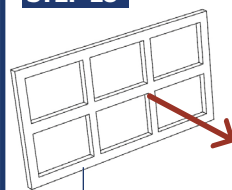
STEP 27



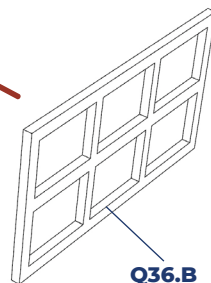
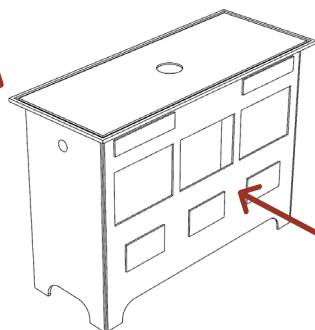
P86



STEP 28

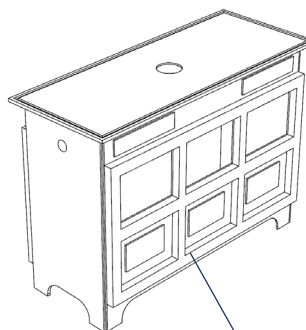


Q36.B

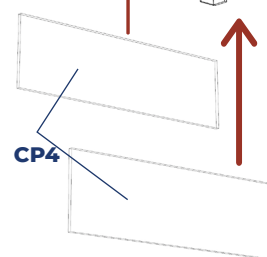
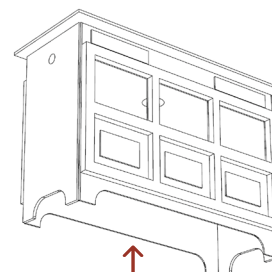


Q36.B

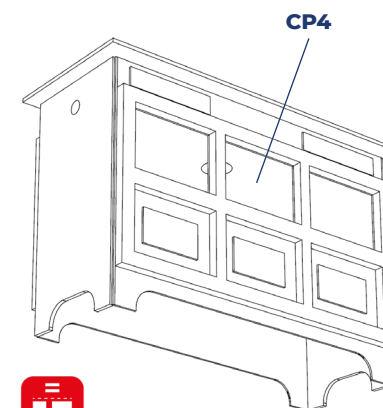
STEP 29



Q36.B



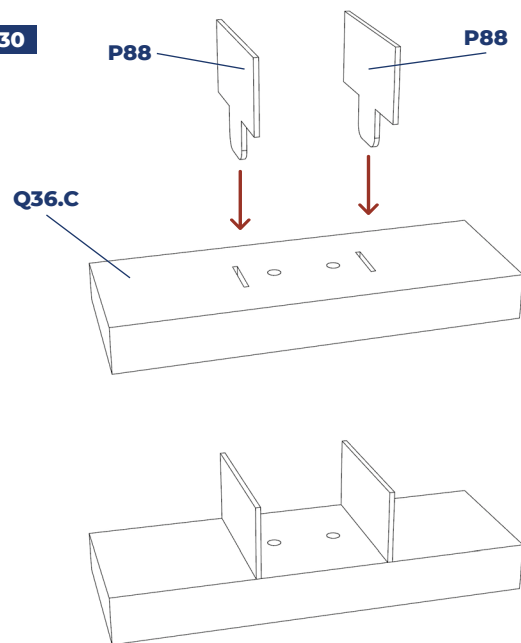
CP4



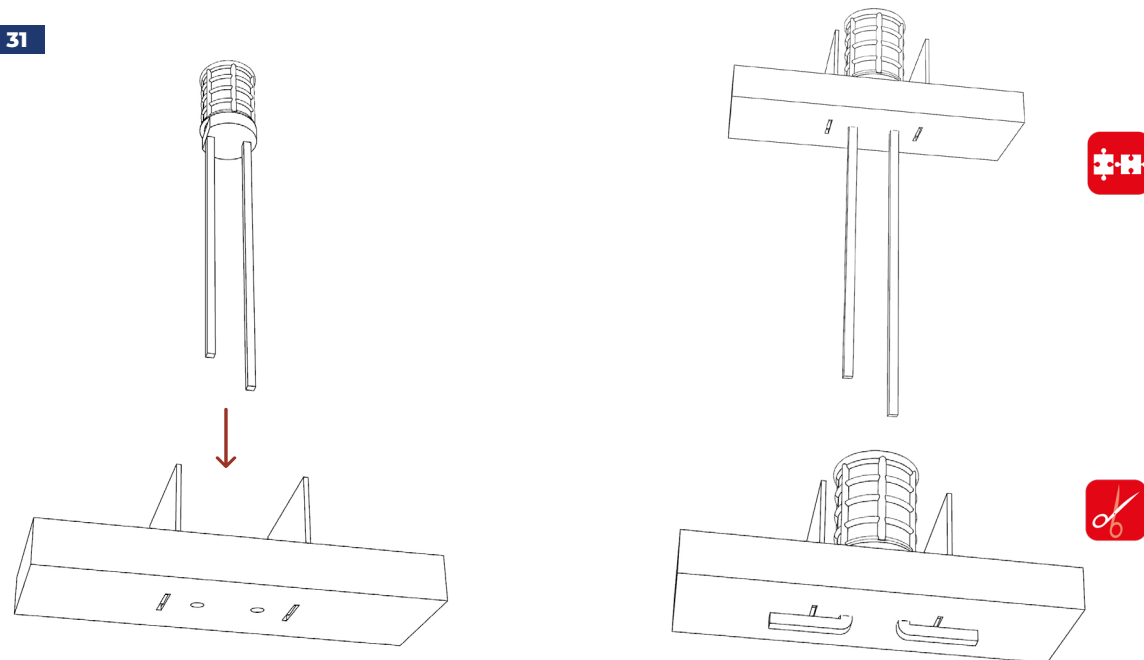
CP4



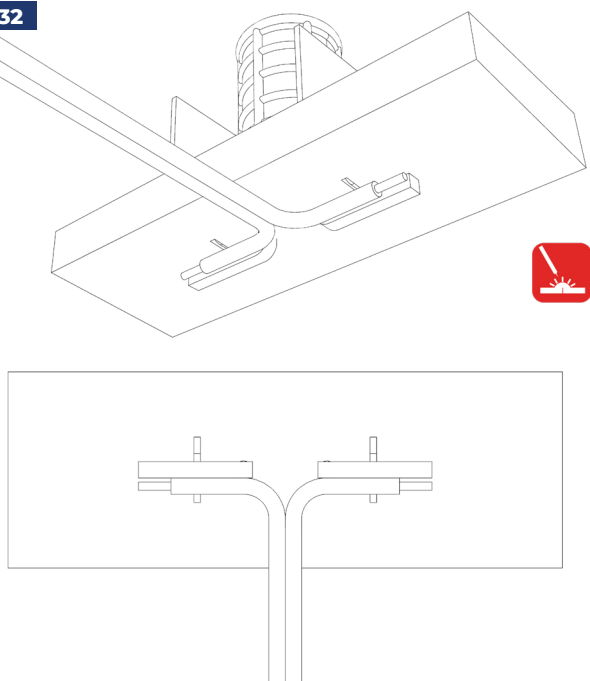
STEP 30



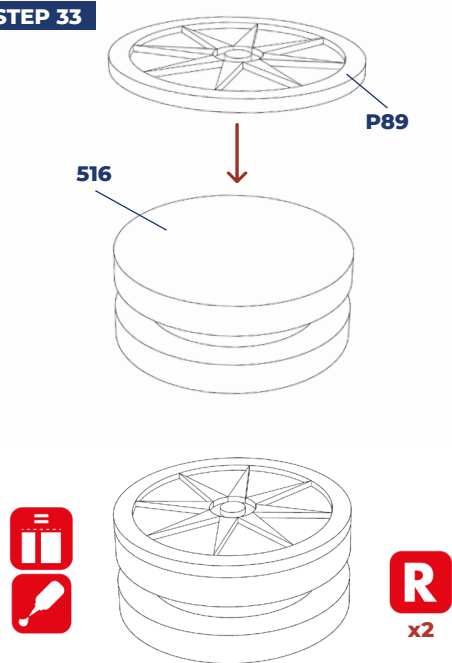
STEP 31



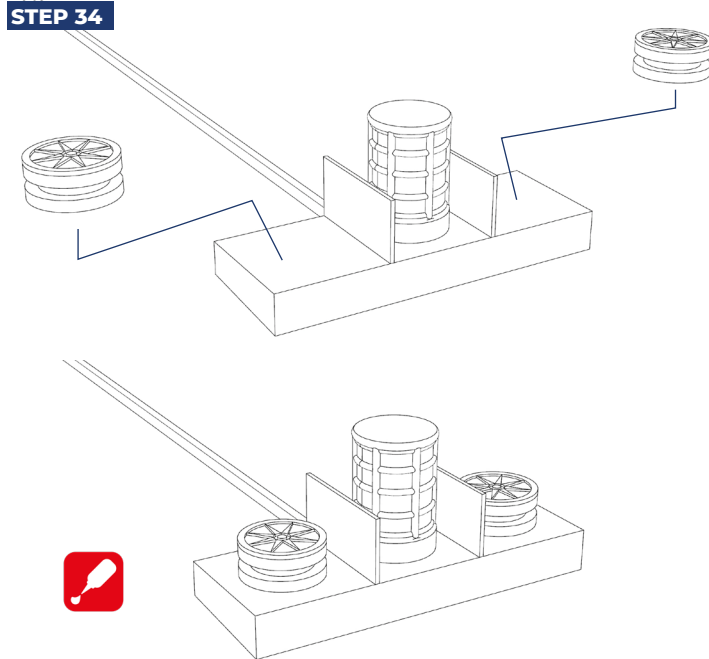
STEP 32



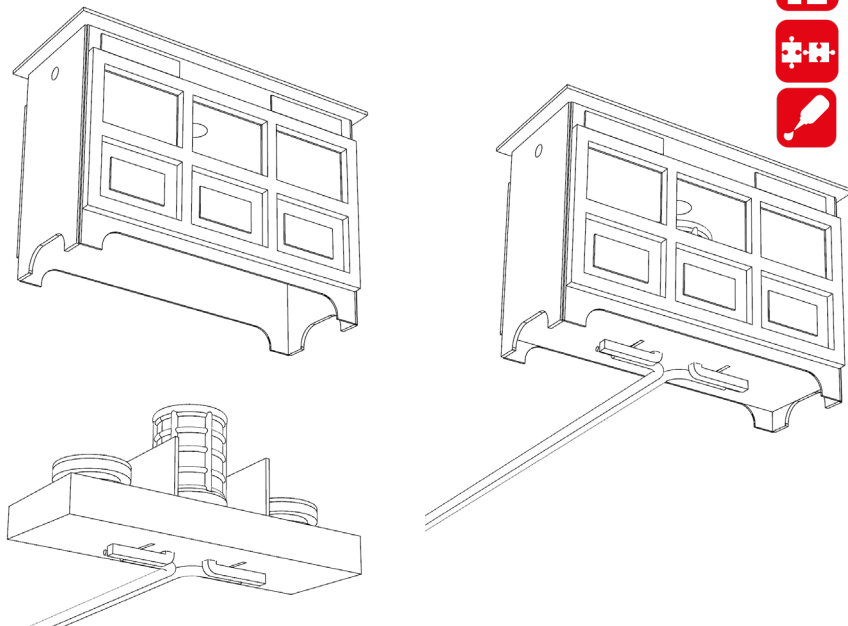
STEP 33



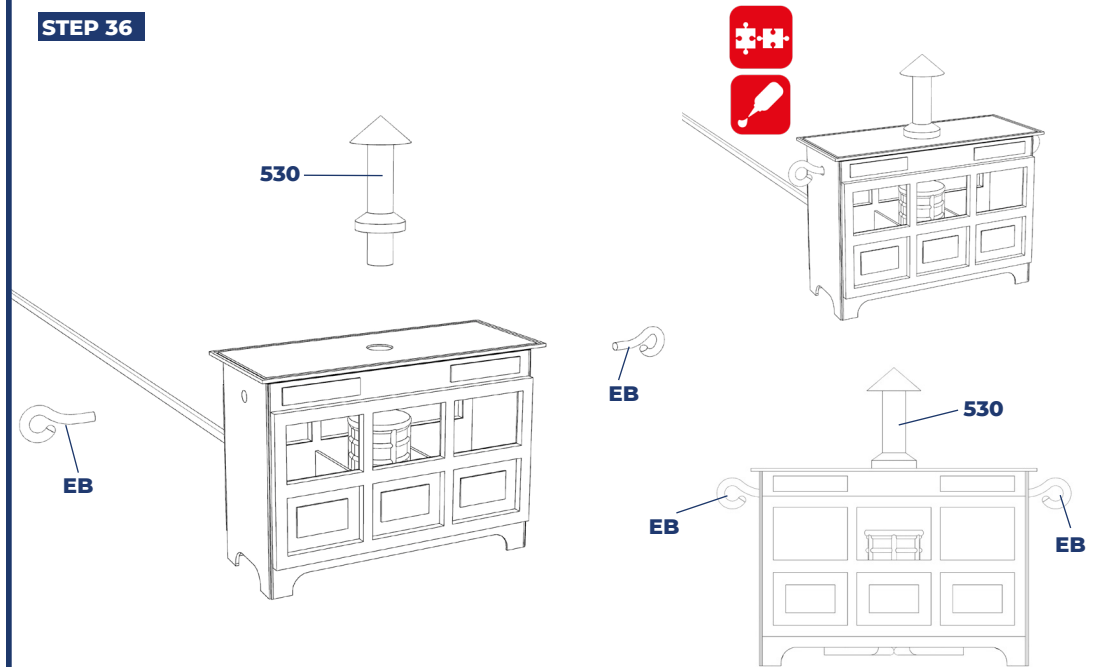
STEP 34



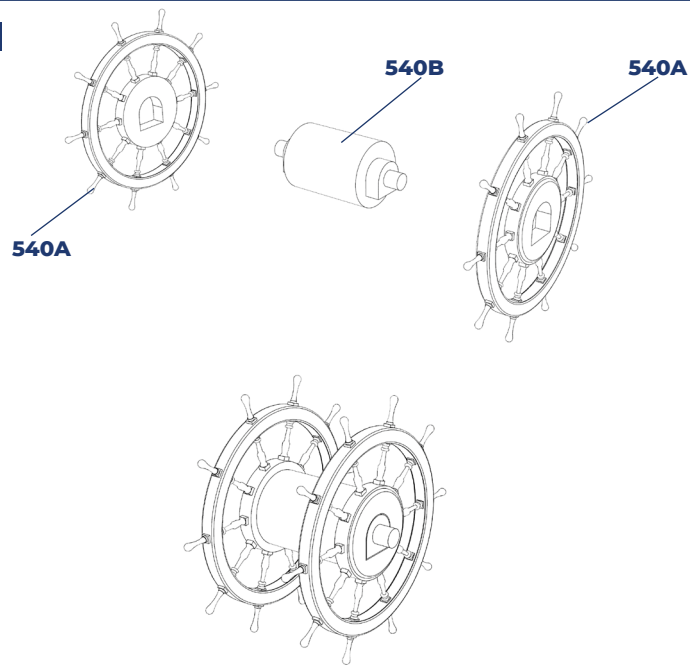
STEP 35



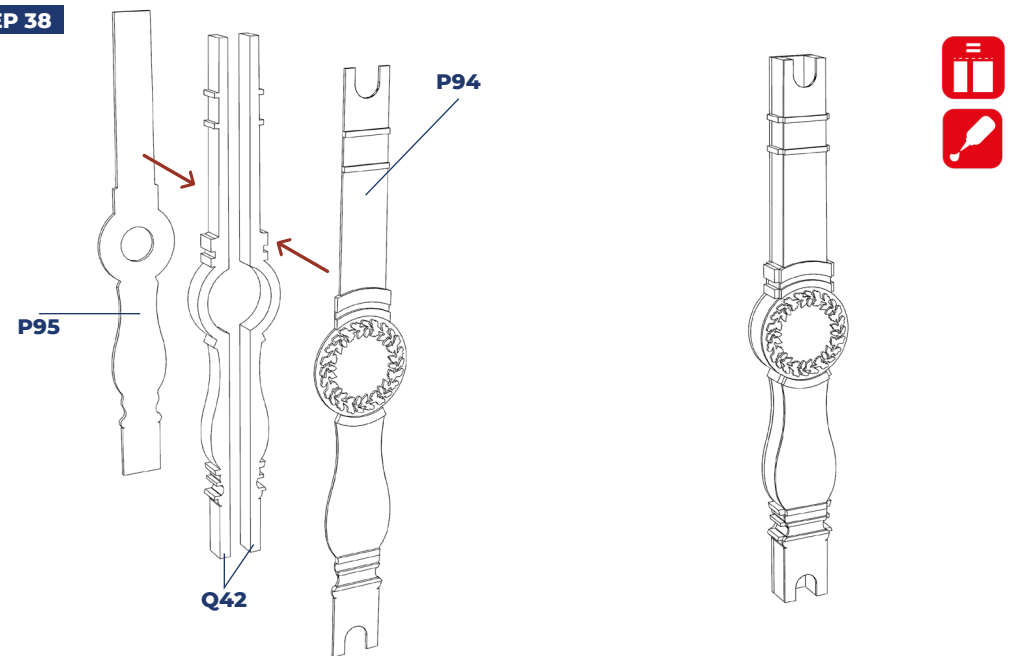
STEP 36



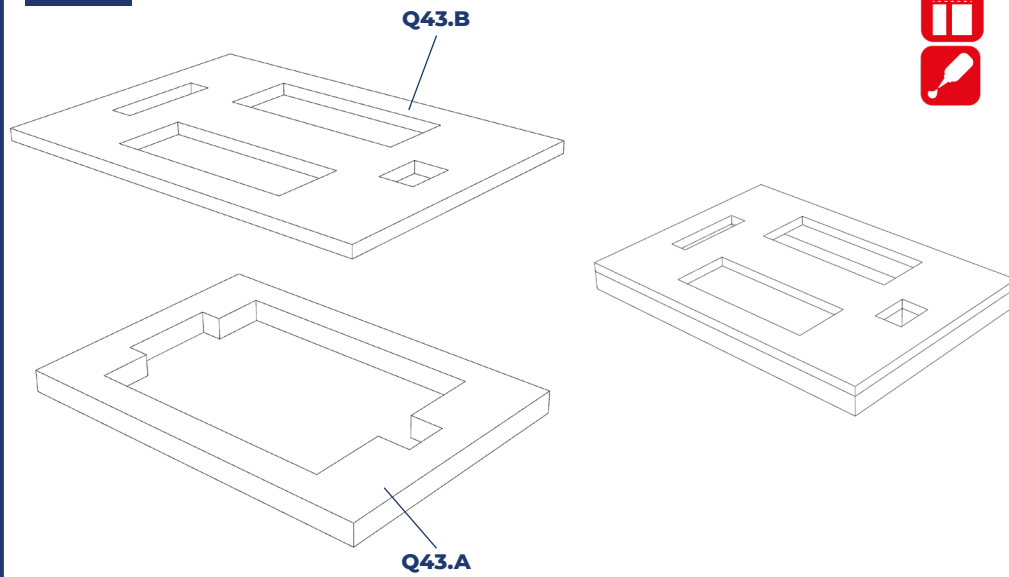
STEP 37



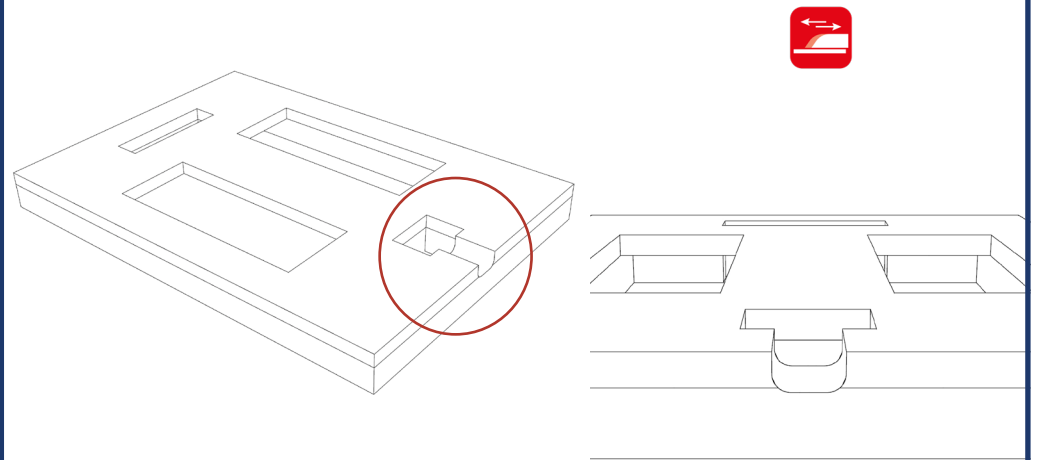
STEP 38



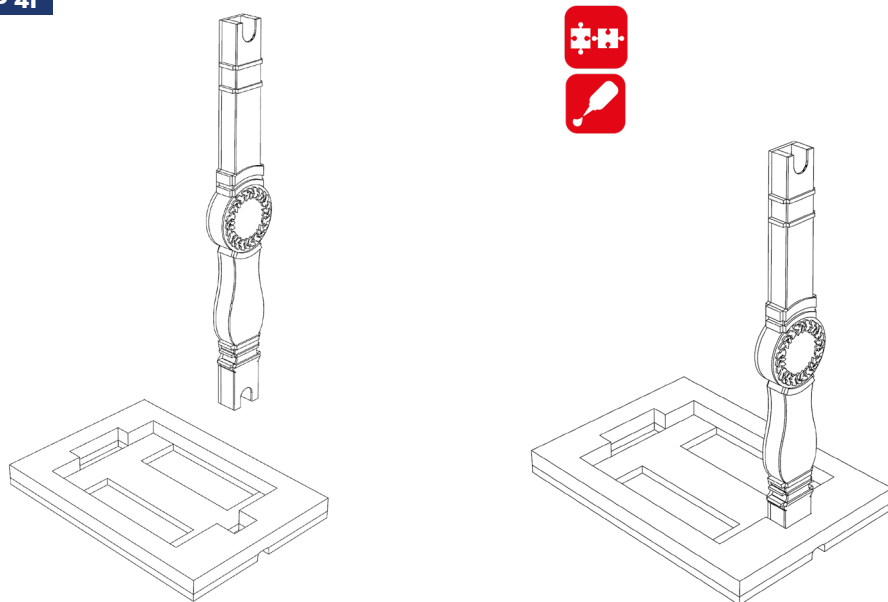
STEP 39



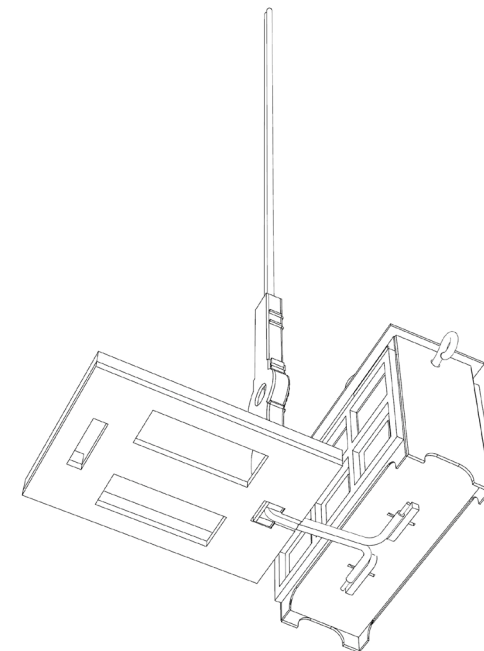
STEP 40



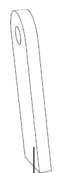
STEP 41



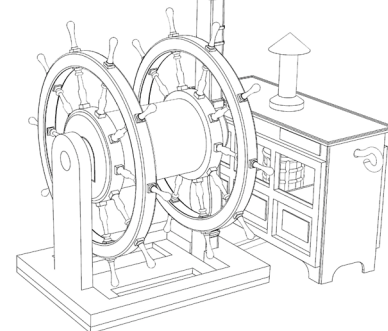
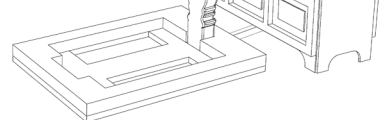
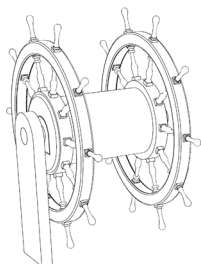
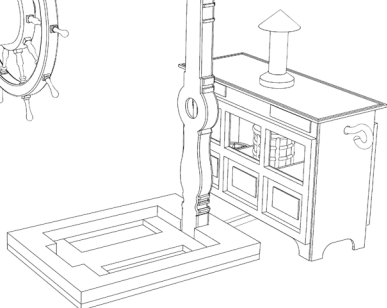
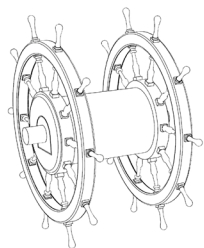
STEP 42



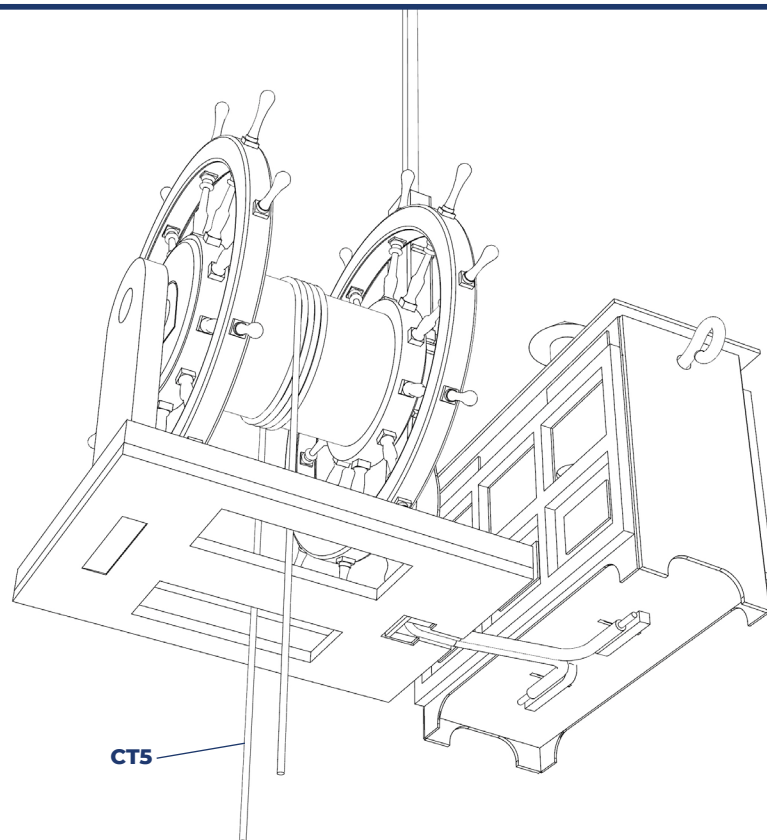
STEP 43



Q44

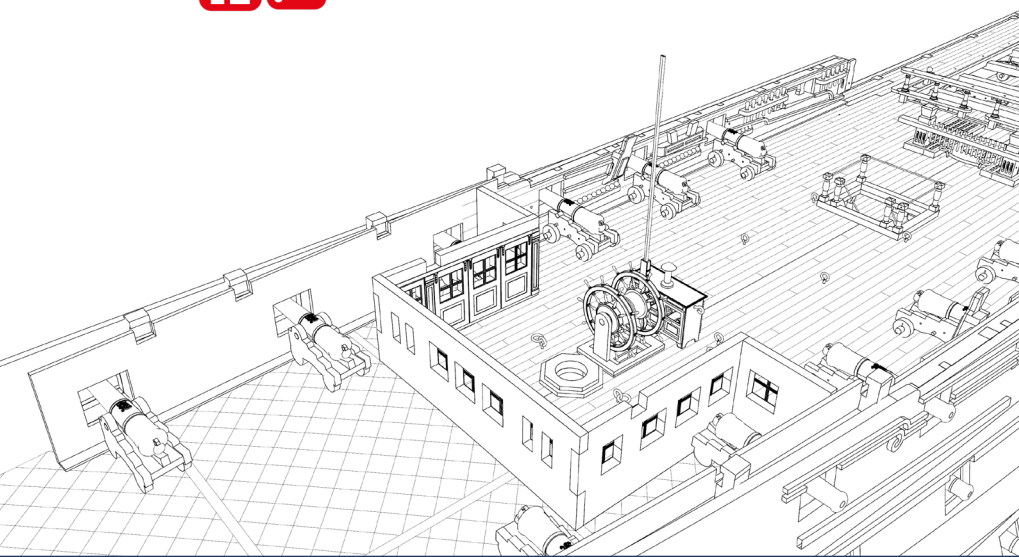
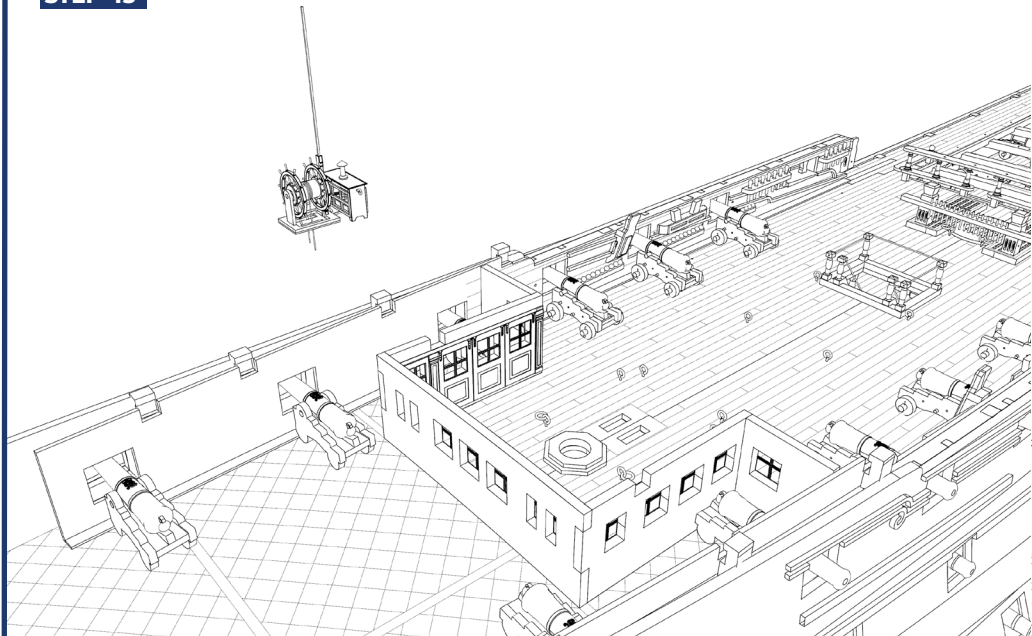


STEP 44



CT5

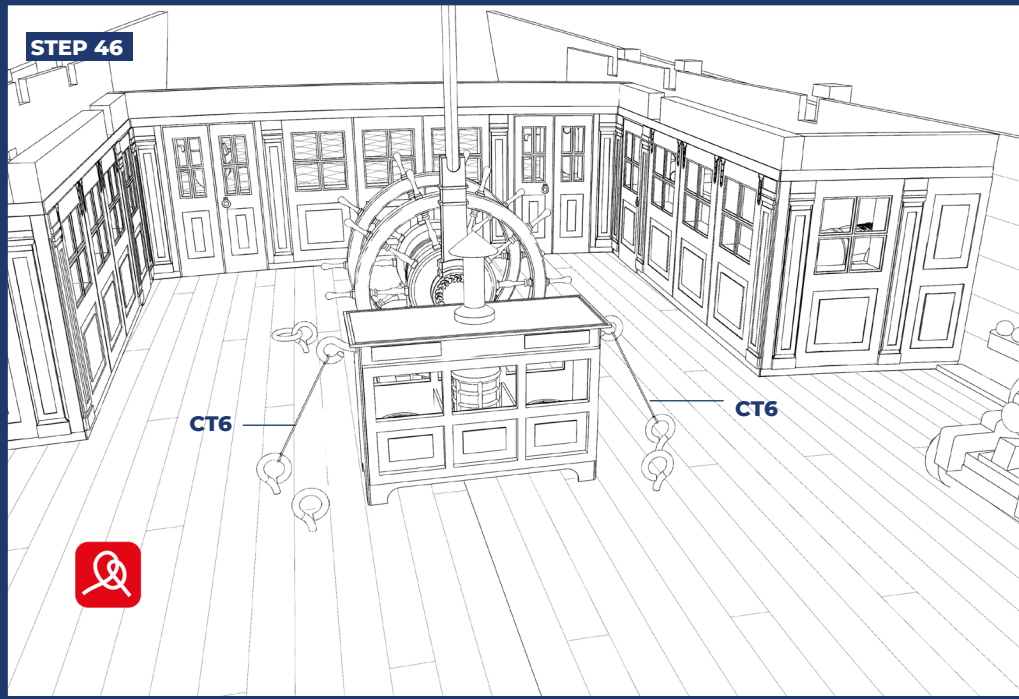
STEP 45



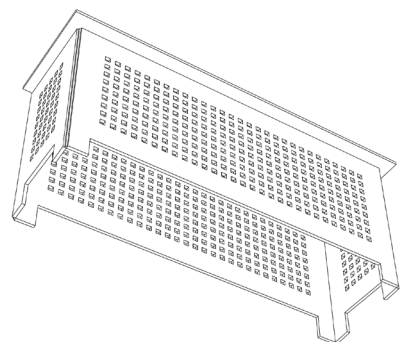
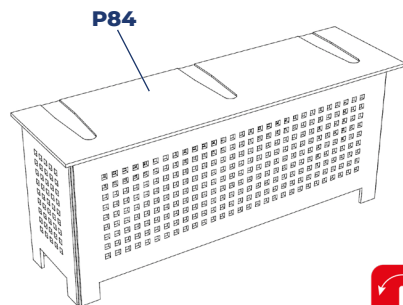
STEP 46

CT6

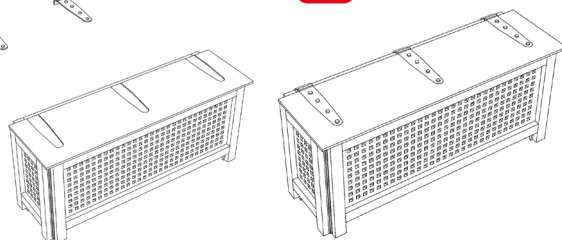
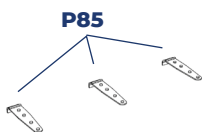
CT6



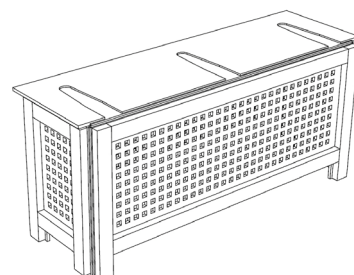
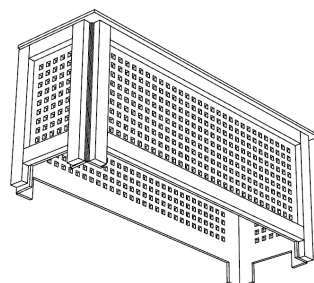
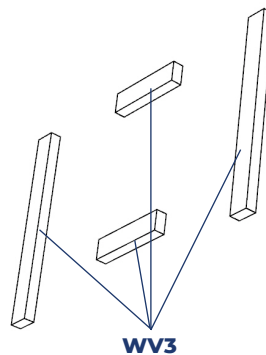
STEP 47



STEP 49



STEP 48



STEP 50

